Stuck-at-a-state is a Boon

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Abstract

I am trying to show stuck-at-a-state is not a hazard or damage for design architecture but can a boon for many cases. I considered different types of image analysis technique, clock rate, clock/data waveform, edge detection and RGB content’s value of an image for this research to carry on. Several performability issues has been taken care for this analysis. I have shown matlab code for simulation. Distortion of an image in case of storing time and restoration time can be taken care differently. By the hardware technique, stuck the bit-value at a level we can correct the error. In order to carry on these types of analysis we are considering the fault tolerance mechanism for dependable computing system.

References

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3. www.matlab.com Matlab

Index Terms

Computer Science Information Sciences

Keywords

RGB, Image Distortion