Abstract

With the upcoming of new innovation in the fields of VLSI and correspondence, there is additionally a perpetually developing interest for fast preparing and low territory outline. It is additionally a verifiable truth that the chip range and most propagation time unit shapes a necessary piece of processor outline. Because of this respect, rapid and low zone designs turn into the need of the day. A fast fourier transform (FFT) is any quick calculation for figuring the DFT. The advancement of FFT calculations tremendously affected computational parts of flag handling and connected science. The decimation in-time (DIT) fast Fourier transform (FFT) all the time has advantage over the decimation in-frequency (DIF) FFT for most genuine esteemed applications, similar to discourse/picture/video handling, biomedical flag preparing, and time-arrangement examination, and so forth., since it doesn't require any yield reordering.

References

1. Pramod Kumar Meher, Basant Kumar Mohanty, Sujit Kumar Patel, Soumya Ganguly, and


Index Terms

Computer Science

Circuits and Systems

Keywords
Efficient VLSI Architecture for DIT and DIF Fast Fourier Transform using Real Valued Data

FFT, MCPD, LUT, Decimation in Time, Decimation in Frequency, real Value data.