Abstract

Reactive power measurement is increasingly paid attention by power industry. A novel architecture to measure the reactive power is proposed in this paper. Architecture is described in verilog and implemented using Xilinx ISE 13.1i. In this method voltage and current signals in time domain are converted to the frequency domain and power on each component is calculated to obtain the total reactive power.

References

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**Index Terms**

Computer Science
Power Electronics

**Keywords**

Xilinx FFT FPGA DFF