Abstract

Linear Prediction Coding (LPC) plays a vital role in speech communication. LPC algorithms are most commonly used for voice coders. In this paper we present a new approach to implement a reconfigurable hardware for sparse LPC algorithms for VoIP applications. The motivation behind this is that the sparser the feature the better would be the bit rate constraint. The necessity of a reconfigurable system is scalability and transcoding. The computational cost is expected to be very low for such a hardware which is capable of solving least square problems.
An Approach to Sparse Reconfigurable Hardware for LPC of Speech for VoIP

References


Index Terms

Computer Science Speech Processing

Keywords

Linear Prediction Coding Reconfigurable Computing Voip